Method for Performing a Domain Transformation of a Digital Signal from the Time Domain into the Frequency Domain and Vice Versa.

Cross Reference to Related Applications

This application claims the benefit of priority of U.S. Provisional Application no 60/507,210, filed 29 September 2003, and U.S. Provisional Application no 60/507,440, filed 29 September 2003, the contents of each being hereby incorporated by reference in its entirety for all purposes.

Further, the following commonly-owned applications are concurrently-filed herewith, and herein incorporated in its entirety:

"Method for Transforming a Digital Signal form the Time Domain into the Frequency Domain and Vice Versa," Atty. Docket no. P100444, and

"Process and Device for Determining a Transforming Element for a Given Transformation Function, Method and Device for Transforming a Digital Signal from the Time Domain into the Frequency Domain and vice versa and Computer Readable Medium," Atty. Docket No. P100452.

Background

This invention relates generally to digital signal processing, and in particular to systems and methods for performing domain transformations of digitally-formatted signals.

Domain transformations, for example the discrete cosine transform (DCT), are widely used in modern signal processing industry. Recently, a variant of the DCT, called integer DCT, has attracted a lot of research interests because of its

important role in lossless coding applications. The term "lossless" means that the decoder can generate an exact copy of the source signal from the encoded bit-stream.

The DCT is a real-valued block transform. Even if the input block consists only of integers, the output block of the DCT can comprise non-integer components. For convenience, the input block is referred to as input vector and the output block as output vector. If a vector comprises only integer components, it is called an integer vector. In contrast to the DCT, the integer DCT generates an integer output vector from an integer input vector. For the same integer input vector, the integer output vector of integer DCT closely approximates the real output vector of DCT. Thus the integer DCT keeps all the good properties of the DCT in spectrum analysis.

An important property of the integer DCT is reversibility. Reversibility means that there exists an integer inverse DCT (IDCT) so that if the integer DCT generates an output vector $\underline{y}$ from an input vector $\underline{x}$, the integer IDCT can recover the vector $\underline{x}$ from the vector $\underline{y}$. Sometimes the integer DCT is also referred to as the forward transform, and the integer IDCT as the backward or inverse transform.

A transform called integer modified discrete cosine transform (IntMDCT) is recently proposed and used in the ISO/IEC MPEG-4 audio compression. The IntMDCT can be derived from its prototype - the modified discrete cosine transform (MDCT). The disclosure by H. S. Malvar in "Signal Processing with Lapped Transforms" *Artech House*, 1992 provides an efficient realization of MDCT by cascading a bank of Givens rotations with a DCT-IV block. It is well known that Givens rotation

can be factorised into three lifting steps for mapping integers to integers. See e.g., R. Geiger, T. Sporer, J. Koller, K. Brandenburg, "Audio Coding based on Integer Transforms" *AES 111th Convention*, New York, USA, Sept. 2001. Integer transforms can be directly converted from their prototypes by replacing each Givens rotation with three lifting steps. Because in each lifting step there is one rounding operation, the total rounding number of an integer transform is three times the Givens rotation number of the prototype transform. For discrete trigonometric transforms (for example the Discrete Fourier Transform (DFT) or the Discrete Cosine Transform (DCT)), the number of Givens rotations involved is usually at $N\log_2 N$ level, where N is the size of the blocks, i.e. the amount of data symbols included in each block, the digital signal is divided into. Accordingly, the total rounding number is also at $N\log_2 N$ level for the family of directly converted integer transforms. Because of the roundings, an integer transform only approximates its floating-point prototype. The approximation error increases with the number of roundings.

What is therefore needed is a method and system implementation for performing domain transformations using fewer rounding operations, thereby resulting in more accurate and less computationally intensive transformations.

Summary of the Invention

The invention solves the problem of providing a method for performing a domain transformation of a digital signal from the time domain into the frequency domain and vice versa which comprises a significantly reduced number of rounding operations.

In one embodiment of the invention, a method for performing a domain transformation of a digital signal from the time domain into the frequency domain and vice versa includes performing the transformation by a transforming element which includes a plurality of lifting stages, wherein the transformation corresponds to a transformation matrix. At least one lifting stage includes at least one auxiliary transformation matrix, which is the transformation matrix itself or the respective transformation matrix of lower dimension. Further, each lifting stage includes a rounding unit. The method further includes processing the signal by the respective rounding unit after the transformation by the respective auxiliary transformation matrix.

Brief Description of the Drawings

Figure 1 shows the architecture of an audio encoder according to an embodiment of the invention;

Figure 2 shows the architecture of an audio decoder according to an embodiment of the invention, which corresponds to the audio coder shown in figure 1;

Figure 3 shows a flow chart of an embodiment of the method according to the invention;

Figure 4 illustrates an embodiment of the method according to the invention using DCT-IV as the transformation function;

Figure 5 illustrates the algorithm for the reverse transformation according to the embodiment of the method according to the invention illustrated in figure 4;

Figure 6 shows a flow chart of an embodiment of the method according to the invention;

Figure 7 illustrates an embodiment of the method according to the invention using DCT-IV as the transformation function;

Figure 8 illustrates the algorithm for the reverse transformation according to the embodiment of the method according to the invention illustrated in figure 7;

Figure 9 shows the architecture for an image archiving system according to an embodiment of the invention;

Figure 10 illustrates an embodiment of the method according to the invention using DWT-IV as the transformation function;

Figure 11 illustrates the algorithm for the reverse transformation according to the embodiment of the method according to the invention illustrated in figure 10;

Figure 12 illustrates a method for performing a domain transformation of a digital signal in accordance with one embodiment of the invention;

Figure 13 shows forward and reverse transform coders used to assess the transform accuracy of presented DCT and FFT transformation methods in accordance with the present invention.

Detailed Description of Specific Embodiments

Figure 12 illustrates a method for performing a domain transformation of a digital signal in accordance with one embodiment of the present invention. Initially at 1210, a transformation of the digital signal is performed by a transforming element comprising a plurality of lifting stages. The transformation corresponds to a transformation matrix wherein at least one of the lifting stages comprises an auxiliary transformation matrix and a rounding unit, the auxiliary matrix comprising the transformation matrix itself of the corresponding transformation matrix of lower dimension. Exemplary embodiments of the process 1210 and domain transformation which can be used in the domain transformation are further described below.

Next at 1220, a rounding operation of the signal is performed by the rounding unit after the transformation by the auxiliary matrix.

According to preferred embodiments of the method according to the invention, the data symbols of the digital signal are provided to the transforming element as a data vector. In each lifting stage, the data vector or a part of the data vector is transformed by a domain transformer, and the result of the transformation is rounded subsequently to an integer vector. This is in contrast to any method according to the state of the art, wherein the rounding process is performed within the transformation process for the individual element or data symbol in each block of the digital signal. Thus, the number of rounding operations in the method according to the invention is greatly reduced. Due to the reduced number of rounding operations, the method according to the invention does not require large computation time and computer resource. Further, the approximation error of integer domain transformations can be significantly reduced.

In a preferred embodiment, the invention provides a method for realizing the integer type-IV DCT transformation. The method according to the invention requires a significantly reduced number of rounding operations compared to prior art methods. As a result, the approximation error is greatly reduced, in the case of DCT-IV it can be reduced from the usual $N\log_2 N$ level to be as low as $1.5N$ in one embodiment and as low as $2.5N$ in another embodiment, where N denotes the block size of the digital signal. The method according to the invention is low in computational complexity and modular in structure.

The method and the device according to the invention can be used for any types of digital signals, such as audio, image or video signals. The digital signal, which is a digitised signal, corresponds to a physical measured signal, may be generated by scanning at least one characteristic feature of a corresponding analog signal (for example, the luminance and chrominance values of a video signal, the amplitude of an analog sound signal, or the analog sensing signal from a sensor). The digital signal comprises a plurality of data symbols. The data symbols of the digital signal are grouped into blocks, with each block having the same predefined number of data symbols based on the sampling rate of the corresponding analog signal.

The method according to the invention can be used for transforming an input digital signal which represents integer values to an output signal which also represents integer values. The transformation method according to the invention is reversible. The output signal may be transformed back to the original input signal by performing the transformation method according to the invention. Such a reversibility property of the transformation according to the method of the invention can be used in lossless coding in which the output signal should be identical to the original input signal.

Such an integer transformation of signals according to the invention can be used in many applications and systems such as MPEG audio, image and video compression, JPEG2000 or spectral analyzers (for analyzing Infrared, Ultra-violet or Nuclear Magnetic Radiation signals). It can also be easily implemented in hardware systems such as in a fixed-point Digital Signal Processor (DSP), without having to consider

factors such as overflow in the case of a real-value signal transformation.

A preferred embodiment of the invention described below is suitable for both mono and stereo applications. In mono applications, two consecutive blocks of samples are grouped and processed together. This introduces signal delay of one block length when compared to single-block processing. However, in stereo applications, this extra block delay can be prevented, if simultaneous sample blocks from the left and the right channel are grouped and processed together.

According to the method according to the invention, the digital signal is transformed to the frequency domain by a transforming element.

Preferably, the transforming element comprises a plurality of lifting stages.

The transforming element can be illustrated based on the model of a lifting ladder. The lifting ladder model has two side pieces, each for receiving one of two groups of data symbols. Two or more cascading lifting stages are provided between the two side pieces. Each lifting stage receives a signal at one end (input end), and outputs a signal at the other end (output end) via a summation unit. A rounding unit is arranged at the output end. The lifting stages are arranged between the side pieces in an alternating manner, such that the output (or input) ends of adjacent lifting stages are connected to the different side pieces.

It should be noted that although the transforming element is described in the form of the lifting ladder model, it is only

to illustrate the transformation paths of the transforming element. However, the invention shall not be limited to said ladder model.

Discrete cosine transforms, discrete sine transforms, discrete Fourier transforms or discrete W transforms are examples of transformations that may be performed by the method according to the invention.

In one aspect of the invention, each lifting stage corresponds to a lifting matrix which is a block-triangular matrix comprising four sub-matrices, with two invertible integer matrices in one diagonal. An example of a lifting matrix is:

$$\begin{bmatrix} \underline{P2_N} & 0 \\ \underline{C_N^{IV}} & \underline{P1_N} \end{bmatrix} \qquad (1)$$

wherein $\underline{C_N^{IV}}$ is the transformation function matrix, in this case the DCT-IV transformation matrix, and $\underline{P1_N}$ and $\underline{P2_N}$ are permutation matrices. A permutation matrix is a matrix which changes the position of the elements in another matrix. The permutation matrices may also be identical. The invertible integer matrices may also be arranged in the lifting matrix in another way, for example as

$$\begin{bmatrix} 0 & \underline{P1_N} \\ \underline{P2_N} & \underline{C_N^{IV}} \end{bmatrix}. \qquad (2)$$

Preferably, the invertible integer matrices are diagonal matrices which comprise only components which are one or minus one.

Figure 1 shows the architecture of an audio encoder 100 according to an embodiment of the invention. The audio encoder 100 comprises a conventional perceptual base layer coder based on the modified discrete cosine transform (MDCT) and a lossless enhancement coder based on the integer modified discrete cosine transform (IntMDCT).

An audio signal 109 which, for instance, is provided by a microphone 110 and which is digitalized by a analog-to-digital converter 111 is provided to the audio encoder 100. The audio signal 109 comprises a plurality of data symbols. The audio signal 109 is divided into a plurality of blocks, wherein each block comprises a plurality of data symbols of the digital signal and each block is transformed by a modified discrete cosine transform (MDCT) device 101. The MDCT coefficients are quantized by a quantizer 103 with the help of a perceptual model 102. The perceptual model controls the quantizer 103 in such a way that the audible distortions resulting from the quantization error are low. The quantized MDCT coefficients are subsequently encoded by a bitstream encoder 104 which produces the lossy perceptually coded output bitstream 112.

The bitstream encoder 104 losslessly compresses its input to produce an output which has a lower average bit-rate than its input by standard methods such as Huffman-Coding or Run-Length-Coding. The input audio signal 109 is also fed into an IntMDCT device 105 which produces IntMDCT coefficients. The quantized MDCT coefficients, which are the output of the quantizer 103, are used to predict the IntMDCT coefficients. The quantized MDCT coefficients are fed into an inverse

quantizer 106 and the output (restored or non-quantized MDCT coefficients) is fed into a rounding unit 107.

The rounding unit rounds to an integer value MDCT coefficients, and the residual IntMDCT coefficients, which are the difference between the integer value MDCT and the IntMDCT coefficients, are entropy coded by an entropy coder 108. The entropy encoder, analogous to the bitstream encoder 104, losslessly reduces the average bit-rate of its input and produces a lossless enhancement bitstream 113. The lossless enhancement bit stream 113 together with the perceptually coded bitstream 112, carries the necessary information to reconstruct the input audio signal 109 with minimal error.

Figure 2 shows the architecture of an audio decoder 200 comprising an embodiment of the invention, which corresponds to the audio coder 100 shown in figure 1. The perceptually coded bitstream 207 is supplied to a bitstream decoder 201, which performs the inverse operations to the operations of the bitstream encoder 104 of Figure 1, producing a decoded bitstream. The decoded bitstream is supplied to an inverse quantizer 202, the output of which (restored MDCT coefficients) is supplied to the inverse MDCT device 203. Thus, the reconstructed perceptually coded audio signal 209 is obtained.

The lossless enhancement bitstream 208 is supplied to an entropy decoder 204, which performs the inverse operations to the operations of the entropy encoder 108 of figure 1 and which produces the corresponding residual IntMDCT coefficients. The output of the inverse quantizer 202 is rounded by a rounding device 205 to produce integer value MDCT coefficients. The integer value MDCT coefficients are

added to the residual IntMDCT coefficients, thus producing the IntMDCT coefficients. Finally, the inverse IntMDCT is applied to the IntMDCT coefficients by an inverse IntMDCT device 206 to produce the reconstructed losslessly coded audio signal 210.

Figure 3 shows a flow chart 300 of an embodiment of the method according to the invention using the DCT-IV as a transformation and using three lifting stages, a first lifting stage 301, a second lifting stage 302 and a third lifting stage 303. This method is preferably used in the IntMDCT device 105 of figure 1 and the inverse IntMDCT device 206 of figure 2 to implement IntMDCT and inverse IntMDCT, respectively. In Fig.3, $\underline{x}_1$ and $\underline{x}_2$ are first and second blocks of the digital signal, respectively. $\underline{z}$ is an intermediate signal, and $\underline{y}_1$ and $\underline{y}_2$ are output signals corresponding to the first and second block of the digital signal, respectively.

As explained above, the DCT-IV-algorithm plays an important role in lossless audio coding.

The transformation function of the DCT-IV comprises the transformation matrix $\underline{C}_N^{IV}$. According to this embodiment of the invention, the transforming element corresponds to a block-diagonal matrix comprising two blocks, wherein each block comprises the transformation matrix $\underline{C}_N^{IV}$.

So, in this embodiment, the matrix corresponding to the transforming element is:

$$\begin{bmatrix} & \underline{C}_N^{IV} \\ \underline{C}_N^{IV} & \end{bmatrix} \qquad (3)$$

$\underline{C_N^{IV}}$ shall in the context of this embodiment be referred to as the transformation matrix henceforth.

The number of lifting matrices, and hence the number of lifting stages in the transformation element, in this embodiment of the invention, wherein DCT-IV is the transformation function, is three.

The DCT-IV of an N-point real input sequence $x(n)$ is defined as follows:

$$y(m) = \sqrt{\frac{2}{N}} \sum_{n=0}^{N-1} x(n) \cos\left(\frac{(m+1/2)(n+1/2)\pi}{N}\right) \quad m,n = 0,1,\cdots,N-1 \tag{4}$$

Let $\underline{C_N^{IV}}$ be the transformation matrix of DCT-IV, that is

$$\underline{C_N^{IV}} = \sqrt{\frac{2}{N}} \left[\cos\left(\frac{(m+1/2)(n+1/2)\pi}{N}\right)\right]_{m,n = 0,1,\cdots,N-1} \tag{5}$$

The following relation holds for the inverse DCT-IV matrix:

$$\left(\underline{C_N^{IV}}\right)^{-1} = \underline{C_N^{IV}} \tag{6}$$

With $\underline{x} = \left[x(n)\right]_{n=0,1,\cdots,N-1}$ and $\underline{y} = \left[y(m)\right]_{m=0,1,\cdots,N-1}$, equation (4) can be expressed as

$$\underline{y} = \underline{C_N^{IV}}\, \underline{x} \tag{7}$$

Now, let $\underline{x_1}$, $\underline{x_2}$ be two integer $N\times1$ column vectors. The column vectors $\underline{x_1}$, $\underline{x_2}$ correspond to two blocks of the digital signal which, according to the invention, are transformed by one transforming element. The DCT-IV transforms of $\underline{x_1}$, $\underline{x_2}$ are $\underline{y_1}$, $\underline{y_2}$, respectively.

$$\underline{y_1} = \underline{C_N^{IV} x_1} \qquad (8)$$

$$\underline{y_2} = \underline{C_N^{IV} x_2} \qquad (9)$$

Combining (8) and (9):

$$\begin{bmatrix} \underline{y_1} \\ \underline{y_2} \end{bmatrix} = \begin{bmatrix} \underline{C_N^{IV}} & \\ & \underline{C_N^{IV}} \end{bmatrix} \begin{bmatrix} \underline{x_1} \\ \underline{x_2} \end{bmatrix} \qquad (10)$$

The above diagonal matrix is the block-diagonal matrix that the transforming element corresponds to.

It is within the scope of the invention if the above equation is changed by simple algebraic modifications like the one leading to

$$\begin{bmatrix} \underline{y_1} \\ \underline{y_2} \end{bmatrix} = \begin{bmatrix} & \underline{C_N^{IV}} \\ \underline{C_N^{IV}} & \end{bmatrix} \begin{bmatrix} \underline{x_2} \\ \underline{x_1} \end{bmatrix} \qquad (11)$$

Let $\underline{T_{2N}}$ be the counter diagonal matrix in (11), that is

$$\underline{T_{2N}} = \begin{bmatrix} & \underline{C_N^{IV}} \\ \underline{C_N^{IV}} & \end{bmatrix} \qquad (12)$$

The matrix $\underline{T_{2N}}$ can be factorised as follows:

$$\underline{T_{2N}} = \begin{bmatrix} & \underline{C_N^{IV}} \\ \underline{C_N^{IV}} & \end{bmatrix} = \begin{bmatrix} \underline{I_N} & \\ -\underline{C_N^{IV}} & \underline{I_N} \end{bmatrix} \begin{bmatrix} -\underline{I_N} & \underline{C_N^{IV}} \\ & \underline{I_N} \end{bmatrix} \begin{bmatrix} \underline{I_N} & \\ \underline{C_N^{IV}} & \underline{I_N} \end{bmatrix} \tag{13}$$

where $\underline{I_N}$ is the $N \times N$ identity matrix.

Equation (13) can be easily verified using the DCT-IV property in (7). Using (13), Equation (11) can be expressed as

$$\begin{bmatrix} \underline{y_1} \\ \underline{y_2} \end{bmatrix} = \begin{bmatrix} \underline{I_N} & \\ -\underline{C_N^{IV}} & \underline{I_N} \end{bmatrix} \begin{bmatrix} -\underline{I_N} & \underline{C_N^{IV}} \\ & \underline{I_N} \end{bmatrix} \begin{bmatrix} \underline{I_N} & \\ \underline{C_N^{IV}} & \underline{I_N} \end{bmatrix} \begin{bmatrix} \underline{x_2} \\ \underline{x_1} \end{bmatrix} \tag{14}$$

The three lifting matrices in equation (14) correspond to the three lifting stages shown in Fig. 3. Each lifting matrix in (14) comprises the auxiliary matrix $\underline{C_N^{IV}}$, which is the transformation matrix itself.

From (14), the following integer DCT-IV algorithm that computes two integer DCT-IVs with one transforming element is derived.

Figure 4 illustrates the embodiment of the method according to the invention using DCT-IV as the transformation function. This embodiment is used in the audio coder 100 shown in Fig. 1 for implementing IntMDCT. Like in Figure 3, $\underline{x_1}$ and $\underline{x_2}$ are two blocks of the input digital signal, $\underline{z}$ is an intermediate signal, and $\underline{y_1}$ and $\underline{y_2}$ are corresponding blocks of the output signal.

The three lifting stages illustrated in figure 4 correspond to the three lifting matrices in equation (14).

As illustrated by figure 4, the time to frequency domain integer transform is determined by the following:

In the first stage 401, $\underline{x_2}$ is transformed by a DCT-IV transformation 402 and the DCT-IV coefficients are rounded 403. The rounded DCT-IV coefficients are then added to $\underline{x_1}$ 404. Thus, the intermediate signal $\underline{z}$ is generated. So, the intermediate signal $\underline{z}$ fulfils the equation:

$$\underline{z} = \lfloor C_N^{IV} \underline{x_2} \rfloor + \underline{x_1} \tag{15a}$$

In the second stage 405, $\underline{z}$ is transformed by a DCT-IV transformation 406 and the DCT-IV coefficients are rounded 407. From the rounded DCT-IV coefficients $\underline{x_1}$ is then subtracted. Thus, the output signal $\underline{y_1}$ is generated. So, the output signal $\underline{y_1}$ fulfils the equation:

$$\underline{y_1} = \lfloor C_N^{IV} \underline{z} \rfloor - \underline{x_2} \tag{15b}$$

In the third stage 409, $\underline{y_1}$ is transformed by a DCT-IV transformation 410 and the DCT-IV coefficients are rounded 411. The rounded DCT-IV coefficients are then subtracted from $\underline{z}$. Thus, the output signal $\underline{y_2}$ is generated. So, the output signal $\underline{y_2}$ fulfils the equation:

$$\underline{y_2} = -\lfloor C_N^{IV} \underline{y_1} \rfloor + \underline{z} \tag{15c}$$

where $\lfloor * \rfloor$ denotes rounding operation.

Figure 5 illustrates the algorithm for the reverse transformation according to an embodiment of the method according to the invention using DCT-IV as the transformation

function. This embodiment is used in the audio decoder 200 shown in Fig. 2 for implementing inverse IntMDCT. The algorithm illustrated in figure 5 is the inverse of the algorithm illustrated in figure 4. The denotations for the different signals $\underline{y_1}$, $\underline{y_2}$, $\underline{x_1}$, $\underline{x_2}$ and $\underline{z}$ are chosen corresponding to the denotations of figure 4.

As illustrated by Figure 5, the frequency to time domain integer transform is determined by the following:

In the first stage 501, $\underline{y_1}$ is transformed by a DCT-IV transformation 502 and the DCT-IV coefficients are rounded 503. The rounded DCT-IV coefficients are then added to $\underline{y_2}$ 504. Thus, the intermediate signal $\underline{z}$ is generated. So, the intermediate signal $\underline{z}$ fulfils the equation:

$$\underline{z} = \lfloor \underline{C_N^{IV} y_1} \rfloor + \underline{y_2} \tag{16a}$$

In the second stage 505, $\underline{z}$ is transformed by a DCT-IV transformation 506 and the DCT-IV coefficients are rounded 507. From the rounded DCT-IV coefficients $\underline{y_1}$ is then subtracted. Thus, the signal $\underline{x_2}$ is generated. So, the signal $\underline{x_2}$ fulfils the equation:

$$\underline{x_2} = \lfloor \underline{C_N^{IV} z} \rfloor - \underline{y_1} \tag{16b}$$

In the third stage 509, $\underline{x_2}$ is transformed by a DCT-IV transformation 510 and the DCT-IV coefficients are rounded 511. The rounded DCT-IV coefficients are then subtracted from $\underline{z}$. Thus, the signal $\underline{x_1}$ is generated. So, the signal $\underline{x_1}$ fulfils the equation:

$$\underline{x}_1 = -\left\lfloor C_N^{IV} \underline{x}_2 \right\rfloor + \underline{z} \tag{16c}$$

It can be seen that the algorithm according to the equations (16a) to (16c) is inverse to the algorithm according to the equations (15a) to (15c). Thus, if used in the encoder and decoder illustrated in figures 1 and 2, the algorithms provide a method and an apparatus for lossless audio coding.

The equations (15a) to (15c) and (16a) to (16c) further show that to compute two $N \times N$ integer DCT-IVs, three $N \times N$ DCT-IVs, three $N \times 1$ roundings, and three $N \times 1$ additions are needed. Therefore, for one $N \times N$ integer DCT-IV, the average is:

$$RC(N) = 1.5N \tag{17}$$

$$AC(N) = 1.5AC(C_N^{IV}) + 1.5N \tag{18}$$

where $RC(.)$ is the total rounding number, and $AC(.)$ is the total number of arithmetic operations. Compared to the directly converted integer DCT-IV algorithms, the proposed integer DCT-IV algorithm reduces RC from level $N\log_2 N$ to N.

As indicated by (18), the arithmetic complexity of the proposed integer DCT-IV algorithm is about 50 percent more than that of a DCT-IV algorithm. However, if RC is also considered, the combined complexity (AC+RC) of the proposed algorithm does not much exceed that of the directly converted integer algorithms. Exact analysis of the algorithm complexity depends on the DCT-IV algorithm used.

As shown in figures 4 and 5, the proposed integer DCT-IV algorithm is simple and modular in structure. It can use any existing DCT-IV algorithms in its DCT-IV computation block. The proposed algorithm is suitable for applications that require IntMDCT, e.g. in the MPEG-4 audio extension 3 reference model 0.

Figure 6 shows a flow chart 600 of an embodiment of the method according to the invention using five lifting stages, a first lifting stage 601, a second lifting stage 602, a third lifting stage 603, a fourth lifting stage 604 and a fifth lifting stage 605. This method can be used in the IntMDCT device 105 of figure 1 and the inverse IntMDCT device 206 of figure 2 to implement IntMDCT and inverse IntMDCT, respectively. In Fig.6, $\underline{x}_1$ and $\underline{x}_2$ are first and second blocks of the digital signal, respectively. $\underline{z}_1$, $\underline{z}_2$, $\underline{z}_3$ are intermediate signals, and $\underline{y}_1$ and $\underline{y}_2$ are output signals corresponding to the first and second block of the digital signal, respectively.

Figure 7 shows a flow chart of an embodiment of the method according to the invention, wherein the transformation function is a DCT-IV transformation function.

The DCT-IV matrix $\underline{C_N^{IV}}$ (see equation (5)) can be factorised as

$$\underline{C_N^{IV}} = \underline{R_{po}}\,\underline{T}\,\underline{P_{eo}} \qquad (20)$$

where $\underline{P_{eo}}$ is an even-odd matrix, i.e., a permutation matrix reordering the components of the vector

$$\underline{x} = \begin{bmatrix} x(0) \\ \vdots \\ x(N-1) \end{bmatrix}$$

by separating the components which correspond to even indices from the components which correspond to odd indices, i.e., such that

$$\begin{bmatrix} x_1(0) \\ \vdots \\ x_1(\frac{N}{2}-1) \\ x_2(0) \\ \vdots \\ x_2(\frac{N}{2}-1) \end{bmatrix} = \underline{P_{eo}} \begin{bmatrix} x(0) \\ \vdots \\ x(N-1) \end{bmatrix} \tag{21}$$

where further

$$\underline{T} = \underline{T_1 T_2 T_3} = \begin{bmatrix} \underline{I_{N/2}} & \\ \underline{K_1} & \underline{I_{N/2}} \end{bmatrix} \cdot \begin{bmatrix} -\underline{D_{N/2}} & \underline{K_2} \\ & \underline{I_{N/2}} \end{bmatrix} \cdot \begin{bmatrix} \underline{I_{N/2}} & \\ \underline{K_3} & \underline{I_{N/2}} \end{bmatrix} \tag{22}$$

with

$$\underline{K_1} = -\left(\underline{C_{N/2}^{IV} D_{N/2}} + \sqrt{2}\underline{I_{N/2}}\right)\underline{C_{N/2}^{IV}} \tag{23}$$

$$\underline{K_2} = \frac{\underline{C_{N/2}^{IV}}}{\sqrt{2}} \tag{24}$$

$$\underline{K_3} = \sqrt{2}\underline{C_{N/2}^{IV} D_{N/2}} + \underline{I_{N/2}} \tag{25}$$

and

$$\underline{R_{po}} = \underline{R_1 R_2 R_3} = \begin{bmatrix} \underline{I_{N/2}} & \\ \underline{H_1} & \underline{I_{N/2}} \end{bmatrix} \cdot \begin{bmatrix} \underline{I_{N/2}} & \underline{H_2} \\ & \underline{I_{N/2}} \end{bmatrix} \cdot \begin{bmatrix} \underline{I_{N/2}} & \\ \underline{H_3} & \underline{I_{N/2}} \end{bmatrix} \tag{26}$$

$$\underline{H}_1 = \underline{H}_3 = \begin{bmatrix} & & & -\tan\frac{(N-1)\pi}{8N} \\ & & \iddots & \\ & -\tan\frac{3\pi}{8N} & & \\ -\tan\frac{\pi}{8N} & & & \end{bmatrix} \tag{27}$$

and

$$\underline{H}_2 = \begin{bmatrix} & & & \sin\frac{\pi}{4N} \\ & & \sin\frac{3\pi}{4N} & \\ & \iddots & & \\ \sin\frac{(N-1)\pi}{4N} & & & \end{bmatrix} \tag{28}$$

Equation (20) can be further written as

$$\underline{C}_N^{IV} = \underline{R_1 R_2 S}\ \underline{T_2 T_3 P_{eo}} \tag{29}$$

where

$$\underline{S} = \underline{R_3 T_1} = \begin{bmatrix} \underline{I}_{N/2} & \\ \underline{H}_3 + \underline{K}_1 & \underline{I}_{N/2} \end{bmatrix} \tag{30}$$

The transforming element used in the embodiment illustrated in figure 7 corresponds to the equation (29). Each lifting matrix *S, T_2 and T_3* in (29) comprises the auxiliary matrix $\underline{C}_{N/2}^{IV}$, which is the transformation matrix itself.

The transforming element comprises five lifting stages which correspond to the five lifting matrices of equation (29).

Further, the transforming element comprises a data shuffling stage corresponding to the permutation matrix $\underline{P}_{eo}$.

In figure 7, the input of the first lifting stage are the two blocks of the digital signal $\underline{x}_1$ and $\underline{x}_2$, $\underline{z}_1$, $\underline{z}_2$ and $\underline{z}_3$ are intermediate signals and $\underline{y}_1$ and $\underline{y}_2$ are output signals corresponding to the first and second block of the digital signal, respectively.

The input of the transforming element $\underline{x}$ and the two input blocks of the first lifting stage of the transforming element $\underline{x}_1$ and $\underline{x}_2$ fulfil the equation

$$\begin{bmatrix} \underline{x}_1 \\ \underline{x}_2 \end{bmatrix} = \underline{P}_{eo}\underline{x} \qquad (31)$$

In the following, the first lifting stage 701 is explained, which is the lifting stage corresponding to the lifting matrix $\underline{T}_3$.

Let $\begin{bmatrix} \underline{v}_1 \\ \underline{v}_2 \end{bmatrix}$ be the output vector of the first lifting stage, for the time being without rounding to integer values, i.e.

$$\begin{bmatrix} \underline{v}_1 \\ \underline{v}_2 \end{bmatrix} = \underline{T}_3 \begin{bmatrix} \underline{x}_1 \\ \underline{x}_2 \end{bmatrix} \qquad (32)$$

Using the definition of $\underline{T}_3$ provided by equation (22), equation (29) is re-written as

$$\begin{bmatrix} \underline{v_1} \\ \underline{v_2} \end{bmatrix} = \begin{bmatrix} \underline{I_{\frac{N}{2}}} & \\ \underline{K_3} & \underline{I_{\frac{N}{2}}} \end{bmatrix} \begin{bmatrix} \underline{x_1} \\ \underline{x_2} \end{bmatrix} \qquad (33)$$

where $\underline{I_{\frac{N}{2}}}$ denotes the identity matrix of dimension N/2.

Since in this embodiment, a reversible algorithm for integer DCT-IV in this provided, there is included a rounding to integer values. So, according to equation (30), in the first step 706 of the first lifting stage 701, $\underline{x_1}$ is multiplied by $\underline{K_3}$. The result of this multiplication is rounded to integer values in step 707. The rounded values are then added to $\underline{x_2}$ in step 708. Thus, the intermediate signal $\underline{z_1}$ fulfils the equation

$$\underline{z_1} = \lfloor \underline{K_3} \cdot \underline{x_1} \rfloor + \underline{x_2} \qquad (34)$$

where $\lfloor * \rfloor$ denotes rounding operation.

Since the other four lifting stages 702, 703, 704, 705 of the transforming element illustrated in figure 7, which correspond to the matrices $\underline{T_2}$, $\underline{S}$, $\underline{R_2}$ and $\underline{R_1}$ respectively, have identical structures as the first lifting stage 701, their explanation is omitted. It only has to be noted that in the adding step 709 of the second lifting step 702, $\underline{x_1}$ is, according to the definition of $\underline{T_2}$, multiplied with $-\underline{D_{N/2}}$.

In the following, the lifting stages of the transforming element of the reverse transformation are explained with reference to figure 8.

Figure 8 illustrates the lifting stages of the transforming element for the reverse transformation of the transformation illustrated in figure 7.

In figure 8, the input of the first lifting stage are the two blocks of the digital signal $\underline{y_1}$ and $\underline{y_2}$, $\underline{z_1}$, $\underline{z_2}$ and $\underline{z_3}$ are intermediate signals and $\underline{x_1}$ and $\underline{x_2}$ are output signals corresponding to the first and second block of the digital signal, respectively.

The last lifting stage 805 illustrated in figure 8 is inverse to the first lifting stage 701 illustrated in figure 7. So, in the first step 806 of the last lifting stage 805, $\underline{x_1}$ is multiplied by $\underline{K_3}$. The result of this multiplication is rounded to integer values in step 807. The rounded values are then subtracted from $\underline{z_1}$ in step 808. Thus, the signal $\underline{x_2}$ fulfils the equation

$$\underline{x_2} = \underline{z_1} - \lfloor \underline{K_3} \cdot \underline{x_1} \rfloor \qquad (35)$$

where $\lfloor * \rfloor$ denotes rounding operation.

Since the other four lifting stages 801, 802, 803, 804 of the transforming element illustrated in figure 8, which are inverse to the lifting stages 705, 704, 703 and 702 of the transforming element illustrated in figure 7 respectively, have identical structures as the last lifting stage 805, their explanation is omitted. It only has to be noted that after the adding step 809 of the fourth lifting step 804, the result of the adding step 809 is multiplied with $-\underline{D_{N/2}}$ to produce $\underline{x_1}$.

It can be seen that the lifting stages 805, 804, 803, 802 and 801 of figure 8 are inverse to the lifting stages 701 to 705 of figure 7, respectively. Since also the permutation of the input signal corresponding to the matrix $\underline{P_{eo}}$ can be reversed and an according data shuffling stage is comprised by the inverse transforming element, the provided method is reversible, thus, if used in the audio encoder 100 and the audio decoder 200 illustrated in figures 1 and 2, providing a method and an apparatus for lossless audio coding.

An analysis of the number of roundings used in this embodiment is given at the end of the description of the invention.

Figure 9 shows the architecture for an image archiving system according to an embodiment of the invention.

In figure 9 an image source 901, for instance a camera, provides an analog image signal. The image signal is processed by a analog-to-digital converter 902 to provide a corresponding digital image signal. The digital image signal is losslessy encoded by a lossless image encoder 903 which includes a transformation from the time domain to the frequency domain. In this embodiment, the time domain corresponds to the coordinate space of the image. The lossless coded image signal is stored in a storage device 904, for example a hard disk or a DVD. When the image is needed, the losslessly coded image signal is fetched from the storage device 904 and provided to a lossless image decoder 905 which decodes the losslessly coded image signal and reconstructs the original image signal without any data loss.

Such lossless archiving of image signals is important, for example, in the case that the images are error maps of semiconductor wafers and have to be stored for later analysis.

In the following, a further embodiment of the method for the transformation of a digital signal from the time domain to the frequency domain and vice versa according to the invention is explained. This embodiment is preferably used in the lossless image encoder 903 and the lossless image decoder 905 of the image archiving system illustrated in figure 9.

Figure 10 illustrates an embodiment of the method according to the invention using DWT-IV as the transformation function.

The DWT-IV of a N-point real input sequence $x(n)$ is defined as follows:

$$y(m) = \sqrt{\frac{2}{N}}\sum_{n=0}^{N-1} x(n)\sin\left(\frac{\pi}{4} + \frac{(m+1/2)(n+1/2)2\pi}{N}\right) \quad m,n = 0,1,\cdots,N-1 \qquad (36)$$

The transformation matrix $\underline{W_N^{IV}}$ of DWT-IV is given by

$$\underline{W_N^{IV}} = \sqrt{\frac{2}{N}}\left[\sin\left(\frac{\pi}{4} + \frac{(m+1/2)(n+1/2)2\pi}{N}\right)\right]_{m,n=0,1,\cdots,N-1} \qquad (37)$$

The DWT-IV matrix is factorised into the following form:

$$\underline{W_N^{IV}} = \underline{R_N T_N P_N} \qquad (38)$$

$\underline{R_N}$ is a $N \times N$ rotation matrix defined as

$$\underline{R_N} = \frac{1}{\sqrt{2}}\begin{bmatrix} \underline{I_{N/2}} & \underline{J_{N/2}} \\ -\underline{J_{N/2}} & \underline{I_{N/2}} \end{bmatrix} \tag{39}$$

$\underline{I_{N/2}}$ is the identity matrix of order N/2. $\underline{J_{N/2}}$ is the counter identity matrix of order N/2, i.e.,

$$\underline{J_{\frac{N}{2}}} = \begin{bmatrix} & & & & 1 \\ & & & 1 & \\ & & 1 & & \\ & \cdot^{\cdot^{\cdot}} & & & \\ 1 & & & & \end{bmatrix} \tag{40}$$

$\underline{P_N}$ is a $N \times N$ permutation matrix

$$\underline{P_N} = \begin{bmatrix} \underline{I_{N/2}} & \\ & \underline{J_{N/2}} \end{bmatrix} \tag{41}$$

$\underline{T}$ is a $N \times N$ matrix given by

$$\underline{T} = \frac{1}{\sqrt{2}}\begin{bmatrix} \underline{C_{N/2}^{IV}} & -\underline{C_{N/2}^{IV}} \\ \underline{C_{N/2}^{IV} D_{N/2}} & \underline{C_{N/2}^{IV} D_{N/2}} \end{bmatrix} \tag{42}$$

where $\underline{C_{N/2}^{IV}}$ is the DCT-IV matrix of order N/2, i.e., a corresponding transformation matrix of lower dimension.

$$\underline{C_{N/2}^{IV}} = \sqrt{\frac{2}{(N/2)}}\left[\cos\left(\frac{(m+1/2)(n+1/2)\pi}{(N/2)}\right)\right]_{m,n=0,1,\cdots,N/2-1} \tag{43}$$

$\underline{D_{N/2}}$ is an order N/2 diagonal matrix given by

$$\underline{D_{N/2}} = \begin{bmatrix} 1 & & & & \\ & -1 & & & \\ & & 1 & & \\ & & & \ddots & \\ & & & & -1 \end{bmatrix} \tag{44}$$

$\underline{R_N}$ and $\underline{T}$ can further be factorised into a product of lifting matrices:

$$\underline{T} = \underline{T_1 T_2 T_3} = \begin{bmatrix} \underline{I_{N/2}} & \\ \underline{K_1} & \underline{I_{N/2}} \end{bmatrix} \cdot \begin{bmatrix} \underline{D_{N/2}} & \underline{K_2} \\ & \underline{I_{N/2}} \end{bmatrix} \cdot \begin{bmatrix} \underline{I_{N/2}} & \\ \underline{K_3} & \underline{I_{N/2}} \end{bmatrix} \tag{45}$$

where $\underline{K_1} = \left(\sqrt{2}\underline{I_{N/2}} - \underline{C_{N/2}^{IV} D_{N/2}}\right)\underline{C_{N/2}^{IV}}$ $\underline{K_2} = \frac{-\underline{C_{N/2}^{IV}}}{\sqrt{2}}$ $\underline{K_3} = \sqrt{2}\underline{C_{N/2}^{IV} D_{N/2}} - \underline{I_{N/2}}$,

and

$$\underline{R_N} = \underline{R_1 R_2 R_3} = \begin{bmatrix} \underline{I_{N/2}} & \\ \underline{H_1} & \underline{I_{N/2}} \end{bmatrix} \cdot \begin{bmatrix} \underline{I_{N/2}} & \underline{H_2} \\ & \underline{I_{N/2}} \end{bmatrix} \cdot \begin{bmatrix} \underline{I_{N/2}} & \\ \underline{H_3} & \underline{I_{N/2}} \end{bmatrix} \tag{46}$$

where

$$\underline{H_1} = \underline{H_3} = -\tan(\pi/8) \cdot \underline{J_{N/2}} = \begin{bmatrix} & & & -0.414 \\ & & -0.414 & \\ & \unicode{x22F0} & & \\ -0.414 & & & \end{bmatrix} \tag{47}$$

and

$$\underline{H_2} = \sin(\pi/4) \cdot \underline{J_{N/2}} = \begin{bmatrix} & & & 0.707 \\ & & 0.707 & \\ & \unicode{x22F0} & & \\ 0.707 & & & \end{bmatrix} \tag{48}$$

Thus, equation (38) can be written in the form

$$\underline{C_N^{IV}} = \underline{R_1 R_2 R_3 T_1 T_2 T_3 P_N} \quad (49)$$

The lifting matrices $\underline{R_3}$ and $\underline{T_1}$ can be aggregated to the lifting matrix $\underline{S}$:

$$\underline{S} = \underline{R_3 T_1} = \begin{bmatrix} \underline{I_{N/2}} & \\ \underline{H_3} + \underline{K_1} & \underline{I_{N/2}} \end{bmatrix} \quad (50)$$

From (49) and (50), the following factorisation formula for the DWT-IV matrix can be obtained:

$$\underline{C_N^{IV}} = \underline{R_1 R_2 S}\ \underline{T_2 T_3 P_N} \quad (51)$$

Equation (51) indicates that a transforming element for the integer DWT-IV transform can be used which comprises five lifting stages. Each lifting matrix S, T_2 *and* T_3 in (51) comprises the auxiliary matrix $\underline{C_{N/2}^{IV}}$, which is the transformation matrix itself.

Further, the transforming element comprises a data shuffling stage corresponding to the permutation matrix $\underline{P_N}$. The data shuffling stage rearranges the components order in each input data block. According to $\underline{P_N}$, the input data vector is rearranged in the following way: the first half of vector remains unchanged; the second half of vector is flipped, i.e.:

$$\underline{P_N}\,\underline{x} = \underline{P_N}\begin{bmatrix} x_1 \\ x_2 \\ \vdots \\ x_{N/2} \\ x_{N/2+1} \\ \vdots \\ x_{N-1} \\ x_N \end{bmatrix} = \begin{bmatrix} x_1 \\ x_2 \\ \vdots \\ x_{N/2} \\ x_N \\ x_{N-1} \\ \vdots \\ x_{N/2+1} \end{bmatrix} \qquad (52)$$

In figure 10, the input of the first lifting stage are the two blocks of the digital signal $\underline{x_1}$ and $\underline{x_2}$, $\underline{z_1}$, $\underline{z_2}$ and $\underline{z_3}$ are intermediate signals and $\underline{y_1}$ and $\underline{y_2}$ are output signals corresponding to the first and second block of the digital signal, respectively.

The input of the transforming element $\underline{x}$ and the two input blocks of the first lifting stage of the transforming element $\underline{x_1}$ and $\underline{x_2}$ fulfil the equation

$$\begin{bmatrix} \underline{x_1} \\ \underline{x_2} \end{bmatrix} = \underline{P_N}\,\underline{x} \qquad (53)$$

In the following, the first lifting stage 1001 is explained, which is the lifting stage corresponding to the lifting matrix $\underline{T_3}$.

Let $\begin{bmatrix} \underline{v_1} \\ \underline{v_2} \end{bmatrix}$ be the output vector of the first lifting stage, for the time being without rounding to integer values, i.e.

$$\begin{bmatrix} \underline{v_1} \\ \underline{v_2} \end{bmatrix} = \underline{T_3}\begin{bmatrix} \underline{x_1} \\ \underline{x_2} \end{bmatrix} \qquad (54)$$

Using the definition of $\underline{T_3}$ provided by equation (45), equation (51) is re-written as

$$\begin{bmatrix} \underline{v_1} \\ \underline{v_2} \end{bmatrix} = \begin{bmatrix} \underline{I_{\frac{N}{2}}} & \\ \underline{K_3} & \underline{I_{\frac{N}{2}}} \end{bmatrix} \begin{bmatrix} \underline{x_1} \\ \underline{x_2} \end{bmatrix} \qquad (55)$$

Since in this embodiment, a reversible algorithm for integer DWT-IV in this provided, there is included a rounding to integer values. So, according to equation (55), in the first step 1006 of the first lifting stage 1001, $\underline{x_1}$ is multiplied by $\underline{K_3}$. The result of this multiplication is rounded to integer values in step 1007. The rounded values are then added to x_2 in step 1008. Thus, the intermediate signal $\underline{z_1}$ fulfils the equation

$$\underline{z_1} = \left\lfloor \underline{K_3} \cdot \underline{x_1} \right\rfloor + \underline{x_2} \qquad (56)$$

where $\lfloor * \rfloor$ denotes rounding operation.

Since the other four lifting stages 1002, 1003, 1004, 1005 of the transforming element illustrated in figure 10, which correspond to the matrices $\underline{T_2}$, $\underline{S}$, $\underline{R_2}$ and $\underline{R_1}$ respectively, have identical structures as the first lifting stage 1001, their explanation is omitted. It only has to be noted that in the adding step 1009 of the second lifting step 1002, $\underline{x_1}$ is, according to the definition of $\underline{T_2}$, multiplied with $\underline{D_{N/2}}$.

In the following, the lifting stages of the transforming element of the reverse transformation are explained with reference to figure 11.

Figure 11 illustrates the lifting stages of the transforming element for the reverse transformation of the transformation illustrated in figure 10.

In figure 11, the input of the first lifting stage are the two blocks of the digital signal $\underline{y}_1$ and $\underline{y}_2$, $\underline{z}_1$, $\underline{z}_2$ and $\underline{z}_3$ are intermediate signals and $\underline{x}_1$ and $\underline{x}_2$ are output signals corresponding to the first and second block of the digital signal, respectively.

The last lifting stage 1105 illustrated in figure 11 is inverse to the first lifting stage 1001 illustrated in figure 10. So, in the first step 1106 of the last lifting stage 1105, $\underline{x}_1$ is multiplied by $\underline{K_3}$. The result of this multiplication is rounded to integer values in step 1107. The rounded values are then subtracted from $\underline{z}_1$ in step 1108. Thus, the signal $\underline{x}_2$ fulfils the equation

$$\underline{x_2} = \underline{z_1} - \lfloor \underline{K_3} \cdot \underline{x_1} \rfloor \tag{57}$$

where $\lfloor * \rfloor$ denotes rounding operation.

Since the other four lifting stages 1101, 1102, 1103, 1104 of the transforming element illustrated in figure 11, which are inverse to the lifting stages 1005, 1004, 1003 and 1002 respectively, have identical structures as the last lifting stage 1105, their explanation is omitted. It only has to be noted that after the adding step 1109 of the fourth lifting step 1104, the result of the adding step 1109 is multiplied with $\underline{D_{N/2}}$ to produce $\underline{x}_1$.

It can be seen that the lifting stages 1105, 1104, 1103, 1102 and 1101 of figure 11 are inverse to the lifting stages 1001 to 1005 of figure 10, respectively. Since also the permutation of the input signal corresponding to the matrix $\underline{P_N}$ can be reversed and an according data shuffling stage is comprised by the inverse transforming element, the whole provided method is reversible, thus, if used in the lossless image encoder 903 and the lossless image decoder 905 illustrated in figure 9, providing a method and an apparatus for lossless image coding.

Although in the explained embodiments, the embodiments of the method according to the invention for DCT-IV were used for audio coding and the embodiment of the method according to the invention for DWT-IV was used for image coding, the embodiments of the method according to the invention for DCT-IV can as well be used for image coding, the embodiment of the method according to the invention for DWT-IV can as well be used audio coding and all can be used as well for the coding of other digital signals, such as video signals.

Considering the equations (34) and (35) one sees that there are $N/2$ roundings in each lifting stage. Therefore, considering equation (26), one sees that the total rounding number for the transforming element for the embodiment of the DCT-IV algorithm illustrated in figures 7 and 8 according to the invention is five times $N/2$, that is $2.5N$, which is significantly lower than $N\log_2 N$ according to the state of the art.

Again considering equation (29), one sees that the majority of computation power is used in the four $N/2$ point DCT-IV subroutines corresponding to a multiplication with $\underline{C_{N/2}^{IV}}$, when

N is a large value, e.g. $N = 1024$. Because the floating-point DCT-IV can be calculated using two half-length DCT-IV plus pre- and post-rotations, the arithmetic complexity of the proposed integer DCT-IV is roughly estimated to be twice that of the floating-point DCT-IV.

A similar conclusion can be drawn for the integer DWT-IV transformation function.

In the following, a further embodiment of the method for the transformation of a digital signal from the time domain to the frequency domain and vice versa according to the invention is explained.

In this embodiment of the present invention, the domain transformation is a DCT transform, whereby the block size N is some integer and where the input vector comprises two sub-vectors.

Let $\mathbf{C}_N^{II}$ be the $N \times N$ transform matrix of DCT (also called Type-II DCT):

$$\mathbf{C}_N^{II} = \sqrt{2/N}\left[k_m \cos(m(n+1/2)\pi/N)\right] \qquad (58)$$
$$m, n = 0, 1, \cdots, N-1$$

where:

$$k_m = \begin{cases} 1/\sqrt{2} & \text{if} \quad m = 0 \\ 1 & \text{if} \quad m \neq 0 \end{cases} \qquad (59)$$

and N is the transform size which can be a power of two, $N = 2^i$ in one embodiment and i is an integer greater than zero. m and n are matrix indices.

Let $\mathbf{C}_N^{IV}$ be the $N \times N$ transform matrix of type-IV DCT, as already defined above:

$$\mathbf{C}_N^{IV} = \sqrt{2/N}\left[\cos\left((m+1/2)(n+1/2)\pi/N\right)\right] \quad m,n = 0,1,\cdots,N-1 \tag{60}$$

As above, a plurality of lifting matrices will be used, which lifting matrices are in this embodiment $2N \times 2N$ matrices of the following form:

$$\mathbf{L}_{2N} = \begin{bmatrix} \pm\mathbf{I}_N & \mathbf{A}_N \\ \mathbf{O}_N & \pm\mathbf{I}_N \end{bmatrix} \tag{61}$$

where $\mathbf{I}_N$ is the $N \times N$ identity matrix, $\mathbf{O}_N$ is the $N \times N$ zero matrix, $\mathbf{A}_N$ is an arbitrary $N \times N$ matrix.

For each lifting matrix $\mathbf{L}_{2N}$, a lifting stage reversible integer to integer mapping is realized in the same way as the 2×2 lifting step described in the incorporated reference "Factoring Wavlet Transforms into Lifting Steps," *Tech. Report*, I. Daubechies and W. Sweldens, Bell Laboratories, Lucent Technologies, 1996. The only difference is that rounding is applied to a vector instead of a single variable.

In the above description of the other embodiments, it was already detailed how a lifting stage is realized for a lifting matrix, so the explanation of the lifting stages corresponding to the lifting matrices will be omitted in the following.

One sees that the transposition of $\mathbf{L}_{2N}$, $\mathbf{L}_{2N}^T$ is also a lifting matrix.

In this embodiment, the transforming element corresponds to a matrix, $\mathbf{T}_{2N}$ which is defined as a $2N \times 2N$ matrix in the following way:

$$\mathbf{T}_{2N} = \begin{bmatrix} \mathbf{C}_N^{IV} & \mathbf{O}_N \\ \mathbf{O}_N & \mathbf{C}_N^{IV} \end{bmatrix} \tag{62}$$

The decomposition of the matrix $\mathbf{T}_{2N}$ into lifting matrices has the following form:

$$\mathbf{T}_{2N} = \mathbf{P3} \cdot \mathbf{L8} \cdot \mathbf{L7} \cdot \mathbf{L6} \cdot \mathbf{P2} \cdot \mathbf{L5} \cdot \mathbf{L4} \cdot \mathbf{L3} \cdot \mathbf{L2} \cdot \mathbf{L1} \cdot \mathbf{P1} \tag{63}$$

The matrices constituting the right hand side of the above equation will be explained in the following.

$\mathbf{P1}$ is a first permutation matrix given by the equation

$$\mathbf{P1} = \begin{bmatrix} \mathbf{O}_N & \mathbf{D}_N \\ \mathbf{J}_N & \mathbf{O}_N \end{bmatrix} \tag{64}$$

where $\mathbf{J}_N$ is the $N \times N$ counter index matrix given by

$$\mathbf{J}_N = \begin{bmatrix} 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \\ 0 & \cdot^{\cdot^{\cdot}} & 0 & 0 \\ 1 & 0 & 0 & 0 \end{bmatrix} \tag{65}$$

and $\mathbf{D}_N$ is a $N \times N$ diagonal matrix with diagonal element being 1 and −1 alternatively:

$$\mathbf{D}_N = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 \\ 0 & -1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & \ddots & 0 \\ 0 & 0 & 0 & 0 & -1 \end{bmatrix} \tag{66}$$

P2 is a second permutation matrix, an example of which is generated by the following MATLAB script:

```
==========================================================
  Pd = eye(2*N);
  for i = 2:2:N,
      Pd(i,i) = 0; Pd(N+i,N+i) = 0;
      Pd(i,N+i) = 1; Pd(N+i,i) = 1;
  end
  Peo = zeros(2*N);
  for i = 1:N,
      Peo(i,2*i-1) = 1;
      Peo(i+N,2*i) = 1;
  end
  P2 = (Pd*Peo)';
==========================================================
```

As an example, when N is 4, **P2** is a 8×8 matrix given as

$$\mathbf{P}_2 = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \end{bmatrix} \quad \text{for} \quad N = 4 \tag{67}$$

P3 is a third permutation matrix, an example of which is generated by the following MATLAB script:

```
==========================================================
P3 = zeros(2*N);
for i = 1:N,
    P3(i,2*i-1) = 1;
    P3(N2-i+1,2*i) = 1;
end
============================================================
```

As an example, when N is 4, **P3** is a 8×8matrix given as

$$\mathbf{P3}=\begin{bmatrix}1&0&0&0&0&0&0&0\\0&0&1&0&0&0&0&0\\0&0&0&0&1&0&0&0\\0&0&0&0&0&0&1&0\\0&0&0&0&0&0&0&1\\0&0&0&0&0&1&0&0\\0&0&0&1&0&0&0&0\\0&1&0&0&0&0&0&0\end{bmatrix}\quad \text{for}\quad N=4 \qquad (68)$$

L1 is a first lifting matrix

$$\mathbf{L1}=\begin{bmatrix}\mathbf{I}_N & \mathbf{O}_N\\ \mathbf{Z1}_N & \mathbf{I}_N\end{bmatrix} \qquad (69)$$

where $\mathbf{Z1}_N$ is a $N\times N$ counter diagonal matrix given as:

$$\mathbf{Z1}_N=\begin{bmatrix}0&0&0&-\tan(\pi/8N)\\0&0&-\tan(3\pi/8N)&0\\0&\cdot^{\cdot^{\cdot}}&0&0\\-\tan((2N-1)\pi/8N)&0&0&0\end{bmatrix} \qquad (70)$$

L2 is a second lifting matrix:

$$\mathbf{L2} = \begin{bmatrix} \mathbf{I}_N & \mathbf{Z2}_N \\ \mathbf{O}_N & \mathbf{I}_N \end{bmatrix} \tag{71}$$

where $\mathbf{Z2}_N$ is a $N \times N$ counter diagonal matrix given as:

$$\mathbf{Z2}_N = \begin{bmatrix} 0 & 0 & 0 & \sin((2N-1)\pi/4N) \\ 0 & 0 & \iddots & 0 \\ 0 & \sin(3\pi/4N) & 0 & 0 \\ \sin(\pi/4N) & 0 & 0 & 0 \end{bmatrix} \tag{72}$$

L3 is a third lifting matrix:

$$\mathbf{L3} = \begin{bmatrix} \mathbf{I}_N & \mathbf{O}_N \\ \mathbf{Z3}_N & \mathbf{I}_N \end{bmatrix} \tag{73}$$

where:

$$\mathbf{Z3}_N = \sqrt{2}\mathbf{C}_N^{IV} + \mathbf{I}_N + \mathbf{Z1}_N \tag{74}$$

L4 is a fourth lifting matrix:

$$\mathbf{L4} = \begin{bmatrix} -\mathbf{I}_N & \mathbf{Z4}_N \\ \mathbf{O}_N & \mathbf{I}_N \end{bmatrix} \tag{75}$$

where:

$$\mathbf{Z4}_N = \mathbf{C}_N^{IV} / \sqrt{2} \tag{76}$$

L5 is a fifth lifting matrix:

$$\mathbf{L5} = \begin{bmatrix} \mathbf{I}_N & \mathbf{O}_N \\ \mathbf{Z5}_N & \mathbf{I}_N \end{bmatrix} \tag{77}$$

where:

$$\mathbf{Z5}_N = -\left(\sqrt{2}\mathbf{C}_N^{IV} + \mathbf{I}_N\right) \quad (78)$$

L6 is a sixth lifting matrix:

$$\mathbf{L6} = \begin{bmatrix} \mathbf{I}_N & \mathbf{O}_N \\ \mathbf{Z6}_N & \mathbf{I}_N \end{bmatrix} \quad (79)$$

where $\mathbf{Z6}_N$ is a $N \times N$ counter diagonal matrix given as:

$$\mathbf{Z6}_N = \begin{bmatrix} 0 & 0 & 0 & \tan(\pi/8) \\ 0 & 0 & \tan(\pi/8) & 0 \\ 0 & \iddots & 0 & 0 \\ \tan(\pi/8) & 0 & 0 & 0 \end{bmatrix} \quad (80)$$

L7 is a seventh lifting matrix:

$$\mathbf{L7} = \begin{bmatrix} \mathbf{I}_N & \mathbf{Z7}_N \\ \mathbf{O}_N & \mathbf{I}_N \end{bmatrix} \quad (81)$$

where $\mathbf{Z7}_N$ is a $N \times N$ counter diagonal matrix given as:

$$\mathbf{Z7}_N = \begin{bmatrix} 0 & 0 & 0 & -\sin(\pi/4) \\ 0 & 0 & \iddots & 0 \\ 0 & -\sin(\pi/4) & 0 & 0 \\ -\sin(\pi/4) & 0 & 0 & 0 \end{bmatrix} \quad (82)$$

L8 is an eighth lifting matrix:

$$\mathbf{L8} = \mathbf{L6} \quad (83)$$

thus, resulting in the factorization as shown in (x):

$$\mathbf{T}_{2N} = \mathbf{P3} \cdot \mathbf{L8} \cdot \mathbf{L7} \cdot \mathbf{L6} \cdot \mathbf{P2} \cdot \mathbf{L5} \cdot \mathbf{L4} \cdot \mathbf{L3} \cdot \mathbf{L2} \cdot \mathbf{L1} \cdot \mathbf{P1} \qquad (84)$$

where **P1**, **P2**, and **P3** are three permutation matrices. $\mathbf{L}j$, j from 1 to 8, are eight lifting matrices.

The lifting matrices **L**3, **L**4 and **L**5 comprise an auxiliary transformation matrix, which is, in this case, the transformation matrix $\mathbf{C}_N^{IV}$ itself.

From Eq. (84), it is possible to compute the integer DCT for two input signals of dimension $N \times 1$.

As Eq. (84) provides a lifting matrix factorization which describes the DCT-IV transformation domain, its lifting matrices can be used in the manner shown herein to compute the domain transformation of an applied input signal.

The equation (84) can be derived in the following way.

The following decomposition can be derived using the disclosure from Wang, Zhongde, "On Computing the Discrete Fourier and Cosine Transforms", IEEE Transactions on Acoustics, Speech and Signal Processing, Vol. ASSP-33, No.4 October 1985:

$$\begin{aligned}\mathbf{C}_N^{IV} &= (\mathbf{B}_N)^T \cdot (\mathbf{P}_N)^T \cdot \begin{bmatrix} \mathbf{C}_{N/2}^{II} & \\ & \overline{\overline{\mathbf{S}_{N/2}^{II}}} \end{bmatrix} \cdot \mathbf{T}_N \\ &= (\mathbf{B}_N)^T \cdot (\mathbf{P}_N)^T \cdot \begin{bmatrix} \mathbf{C}_{N/2}^{II} & \\ & \mathbf{C}_{N/2}^{II} \end{bmatrix} \cdot \mathbf{P}_{DJ} \cdot \mathbf{T}_N \end{aligned} \tag{85}$$

is known, wherein $\mathbf{S}_{N/2}^{II}$ denotes the transformation matrix of the discrete sine transform of type 2,

$$\mathbf{P}_{DJ} = \begin{bmatrix} \mathbf{I} & \\ & \mathbf{D} \cdot \mathbf{J} \end{bmatrix}$$

$\underline{P_N}$ is a $N \times N$ permutation matrix given by

$$\underline{P_N} = \begin{bmatrix} \underline{I_{N/2}} & \\ & \underline{J_{N/2}} \end{bmatrix} \tag{86}$$

$$\underline{T_N} = \begin{bmatrix} \cos\frac{\pi}{4N} & & & & & & & \sin\frac{\pi}{4N} \\ & \cos\frac{3\pi}{4N} & & & & & \sin\frac{3\pi}{4N} & \\ & & \ddots & & & \iddots & & \\ & & & \cos\frac{(N-1)\pi}{4N} & \sin\frac{(N-1)\pi}{4N} & & & \\ & & & -\sin\frac{(N-1)\pi}{4N} & \cos\frac{(N-1)\pi}{4N} & & & \\ & & \iddots & & & \ddots & & \\ & -\sin\frac{3\pi}{4N} & & & & & \cos\frac{3\pi}{4N} & \\ -\sin\frac{\pi}{4N} & & & & & & & \cos\frac{\pi}{4N} \end{bmatrix}$$

and

$$B_N = \frac{1}{\sqrt{2}}\begin{bmatrix} \sqrt{2} & & & & & & & \\ & 1 & -1 & & & & & \\ & 1 & 1 & & & & & \\ & & & \cdot & & & & \\ & & & & \cdot & & & \\ & & & & & \cdot & & \\ & & & & & & 1 & -1 & \\ & & & & & & 1 & 1 & \\ & & & & & & & & \sqrt{2} \end{bmatrix}$$

Equation (85) can be combined with the equation

$$\mathbf{C}_N^{IV} = \mathbf{R}_{PO} \cdot \begin{bmatrix} \mathbf{C}_{N/2}^{IV} & \\ & \mathbf{C}_{N/2}^{IV} \end{bmatrix} \cdot \mathbf{R}_{PR} \cdot \mathbf{P}_D \cdot \mathbf{P}_{EO} \qquad (87)$$

wherein P_{EO} is an even-odd permutation matrix,

$$\underline{R_{pr}} = \frac{1}{\sqrt{2}}\begin{bmatrix} \underline{I_{N/2}} & \underline{I_{N/2}} \\ \underline{I_{N/2}} & -\underline{I_{N/2}} \end{bmatrix}$$

R_{PO} equals T_N,

$$\underline{P}_D = \begin{bmatrix} \underline{I_{\frac{N}{2}}} & \\ & \underline{D_{\frac{N}{2}}} \end{bmatrix}$$

After transposition equation (87) converts to

$$\begin{aligned} \mathbf{C}_N^{IV} &= (\mathbf{P}_{EO})^T \cdot (\mathbf{P}_D)^T \cdot \mathbf{R}_{PR} \cdot \begin{bmatrix} \mathbf{C}_{N/2}^{IV} & \\ & \mathbf{C}_{N/2}^{IV} \end{bmatrix} \cdot (\mathbf{R}_{PO})^T \\ &= (\mathbf{P}_{EO})^T \cdot (\mathbf{P}_D)^T \cdot \frac{1}{\sqrt{2}} \begin{bmatrix} \mathbf{C}_{N/2}^{IV} & \mathbf{C}_{N/2}^{IV} \\ \mathbf{C}_{N/2}^{IV} & -\mathbf{C}_{N/2}^{IV} \end{bmatrix} \cdot (\mathbf{R}_{PO})^T \end{aligned} \qquad (88)$$

The combination of (85) and (88) yields

$$\begin{bmatrix} \mathbf{C}_{N/2}^{II} & \\ & \mathbf{C}_{N/2}^{II} \end{bmatrix} = \mathbf{P}_N \cdot \mathbf{B}_N \cdot (\mathbf{P}_{EO})^T \cdot (\mathbf{P}_D)^T \cdot \frac{1}{\sqrt{2}} \begin{bmatrix} \mathbf{C}_{N/2}^{IV} & \mathbf{C}_{N/2}^{IV} \\ \mathbf{C}_{N/2}^{IV} & -\mathbf{C}_{N/2}^{IV} \end{bmatrix} \cdot (\mathbf{R}_{PO})^T \cdot \mathbf{T}_N \cdot (\mathbf{P}_{DJ})^T$$
$$= \mathbf{P}_3 \cdot \mathbf{R}_2 \cdot \mathbf{P}_2 \cdot \frac{1}{\sqrt{2}} \begin{bmatrix} \mathbf{C}_{N/2}^{IV} & \mathbf{C}_{N/2}^{IV} \\ \mathbf{C}_{N/2}^{IV} & -\mathbf{C}_{N/2}^{IV} \end{bmatrix} \cdot \mathbf{R}_1 \cdot \mathbf{P}_1 \qquad (89)$$

where

$$\mathbf{P}_1 = (\mathbf{P}_{DJ})^T$$
$$\mathbf{P}_2 = (\mathbf{P}_{EO})^T \cdot (\mathbf{P}_D)^T = (\mathbf{P}_D \cdot \mathbf{P}_{EO})^T$$
$$\mathbf{P}_3 = \mathbf{P}_N$$
$$\mathbf{R}_1 = (\mathbf{R}_{PO})^T \cdot \mathbf{T}_N$$
$$\mathbf{R}_2 = \mathbf{B}_N$$

from (89), equation (84) can be easily derived.

In this embodiment, the computation of the domain transformation requires only 4N rounding operations, as will now be explained:

Let $\alpha(*)$ be the number of real additions, $\mu(*)$ be the number of real multiplications, and $\gamma(*)$ be the number of real roundings, respectively. For the proposed IntDCT algorithm, one gets:

$$\alpha(\text{IntDCT}) = 11N + 3\alpha(\text{DCT-IV})$$
$$\mu(\text{IntDCT}) = 9N + 3\mu(\text{DCT} - \text{IV})$$
$$\gamma(\text{IntDCT}) = 8N$$

The above results are for two blocks of data samples, because the proposed IntDCT algorithm processes them together. Thus

for one block of data samples, the numbers of calculations are halved, which are

$$\alpha_1(\text{IntDCT}) = 5.5N + 1.5\alpha(\text{DCT-IV})$$
$$\mu_1(\text{IntDCT}) = 4.5N + 1.5\mu(\text{DCT}-\text{IV})$$
$$\gamma_1(\text{IntDCT}) = 4N$$

where α_1, μ_1, and γ_1 are the number of real additions, number of real multiplications, and number of real roundings, for one block of samples, respectively.

For DCT-IV calculation, the FFT-based algorithm described in the incorporated reference "Signal Processing with lapped Transforms," H. S. Malvar, Norwood, MA. Artech House, 1992, pp. 199-201 can be used, for which

$$\alpha(\text{DCT}-\text{IV}) = 1.5N\log_2 N$$
$$\mu(\text{DCT}-\text{IV}) = 0.5N\log_2 N + N$$

Consequently:

$$\alpha_1(\text{IntDCT}) = 2.25N\log_2 N + 5.5N$$
$$\mu_1(\text{IntDCT}) = 0.75N\log_2 N + 6N$$

In the following, a further embodiment of the method for the transformation of a digital signal from the time domain to the frequency domain and vice versa according to the invention is explained.

In this embodiment a discrete fast fourier transform (FFT) is used as the domain transformation.

Let $\mathbf{F}$ be the $N \times N$ transform matrix of the normalized FFT

$$\mathbf{F} = \sqrt{\frac{1}{N}}\left[\exp\left(\frac{-j2\pi mn}{N}\right)\right]$$
$$m,n = 0,1,\cdots,N-1 \tag{91}$$

where N is the transform size which is some positive integer and, in one embodiment, can be a power of two, $N = 2^i$ and i is an integer greater than zero. m and n are matrix indices.

Under this embodiment, a permutation matrix $\mathbf{P}$ of dimension $N \times N$ is a matrix which includes indices 0 or 1. After multiplying it with a $N \times 1$ vector (the matrix representation of the input signal), the order of elements in the vector are changed.

In this embodiment, lifting matrices are defined as $2N \times 2N$ matrices of the following form:

$$\mathbf{L} = \begin{bmatrix} \mathbf{P}_1 & \mathbf{A} \\ \mathbf{O} & \mathbf{P}_2 \end{bmatrix} \tag{92}$$

where $\mathbf{P}_1$ and $\mathbf{P}_2$ are two permutation matrices, $\mathbf{O}$ is the $N \times N$ zero matrix, $\mathbf{A}$ is an arbitrary $N \times N$ matrix. For lifting matrix $\mathbf{L}$, reversible integer to integer mapping is realized in the same way as the 2×2 lifting step in the aforementioned incorporated reference of I. Daubechies. As above, however, rounding is applied to a vector instead of a single variable. It is apparent that the transposition of $\mathbf{L}$, $\mathbf{L}^T$ is also a lifting matrix.

Further, let $\mathbf{T}$ be a $2N \times 2N$ transform matrix:

$$\mathbf{T} = \begin{bmatrix} \mathbf{O} & \mathbf{F} \\ \mathbf{F} & \mathbf{O} \end{bmatrix} \quad (93)$$

Accordingly, the modified transform matrix $\mathbf{T}$ (and accordingly the domain transformation itself) can be expressed as the lifting matrix factorization:

$$\mathbf{T} = \begin{bmatrix} \mathbf{I} & \mathbf{O} \\ -\mathbf{Q}\cdot\mathbf{F} & \mathbf{I} \end{bmatrix} \cdot \begin{bmatrix} -\mathbf{Q} & \mathbf{F} \\ \mathbf{O} & \mathbf{I} \end{bmatrix} \cdot \begin{bmatrix} \mathbf{I} & \mathbf{O} \\ \mathbf{F} & \mathbf{I} \end{bmatrix} \quad (94)$$

where $\mathbf{I}$ is the $N \times N$ identity matrix, and $\mathbf{Q}$ is a $N \times N$ permutation matrix given as:

$$\mathbf{Q} = \begin{bmatrix} 1 & \mathbf{O}_{1 \times N-1} \\ \mathbf{O}_{N-1 \times 1} & \mathbf{J} \end{bmatrix} \quad (95)$$

and $\mathbf{O}_{1 \times N-1}$ and $\mathbf{O}_{N-1 \times 1}$ are row and column vectors of $N-1$ zeros respectively.

$\mathbf{J}$ is the $(N-1)\times(N-1)$ counter index matrix given by

$$\mathbf{J} = \begin{bmatrix} & & & 1 \\ & & 1 & \\ & \iddots & & \\ 1 & & & \end{bmatrix} \quad (96)$$

In Eq. (96), blank space in the square bracket represents all zeros matrix elements.

The lifting matrices on the right hand side of equation (94) comprise an auxiliary transformation matrix, which is, in this case, the transformation matrix F itself.

A can be seen from Eq. (94), the lifting matrix factorization can be use to compute the integer FFT for two $N \times 1$ complex vectors using the methods as described herein.

Under this embodiment, the computation of the domain transformation requires only 3N rounding operations, as will now be explained:

Let: $\alpha(*)$ be the number of real additions,

$\mu(*)$ be the number of real multiplications, and

$\gamma(*)$ be the number of real rounding operations,

respectively.

For the proposed IntFFT algorithm, we have

$$\alpha(\mathrm{IntFFT}) = 6N + 3\alpha(\mathrm{FFT})$$
$$\mu(\mathrm{IntFFT}) = 3\mu(\mathrm{FFT})$$
$$\gamma(\mathrm{IntFFT}) = 6N$$

The above results are for two blocks of data samples, because the proposed IntFFT algorithm processes them together. Thus for one block of data samples, the numbers of calculations are halved, which are

$$\alpha_1(\mathrm{IntFFT}) = 3N + 1.5\alpha(\mathrm{FFT})$$

$$\mu_1(\text{IntFFT}) = 1.5\mu(\text{FFT})$$

$$\gamma_1(\text{IntFFT}) = 3N$$

where α_1, μ_1, and γ_1 are the number of real additions, number of real multiplications, and number of real rounding opreations for one block of samples, respectively.

For FFT calculation, the split-radix FFT (SRFFT) algorithm can be used, for which:

$$\alpha(\text{SRFFT}) = 3N\log_2 N - 3N + 4$$

$$\mu(\text{SRFFT}) = N\log_2 N - 3N + 4$$

Consequently, we have:

$$\alpha_1(\text{IntFFT}) = 4.5N\log_2 N - 1.5N + 6$$

$$\mu_1(\text{IntFFT}) = 1.5N\log_2 N - 4.5N + 6$$

Figure 13 shows forward and reverse transform coders used to assess the transform accuracy of the DCT transformation technique described above and the FFT domain transformation above. The test involved measuring the mean squared error (MSE) of the transform in accordance with the evaluation standards proposed by the MPEG-4 lossless audio coding group as described in "Coding of Moving Pictures and Audio: Work plan for Evaluation of Integer MDCT for FGS to Lossless Experimentation Framework," ISO/IEC JTC 1/SC 29/WG 11 N5578 Pattaya, Thailand, Mar. 2003 incorporated herein.

Specifically, the MSEs for IntDCT and integer inverse DCT (IntIDCT) are given as:

$$MSE = \frac{1}{K}\sum_{j=0}^{K-1}\frac{1}{N}\sum_{i=0}^{N-1}e_i^2 \qquad (97)$$

where the error signal e is e_f for IntDCT, and e_i for IntIDCT as in Fig 1. K is the total number of sample blocks used in the evaluation.

The MSEs for IntFFT and integer inverse FFT (IntIFFT) are given as

$$MSE = \frac{1}{K}\sum_{j=0}^{K-1}\frac{1}{N}\sum_{i=0}^{N-1}\|e_i\|^2 \qquad (98)$$

where the error signal e is e_f for IntFFT, and e_i for IntIFFT as in Fig 1. $\|*\|$ represents norm of a complex value. K is the total number of sample blocks used in the evaluation.

For both domain transformations, a total of 450 seconds with 15 different types of music files are used in the 48 kHz/16-bit test set. Table I shows the test results.

As can be seen from Table 1, the MSE generated using the systems and methods of the present invention is very minimal, and unlike conventional systems, is substantially independent of the processing block size. Referring to the DCT-IV domain transformation, the MSE only slightly increases with increasing block size N up to 4096 bits. The MSEs of the FFT are even better, exhibiting a constant MSE of 0.4 for block sizes up to 4096 bits. When the demonstrated performance of the present invention is viewed in light of the present capabilities and increasing need for longer block sizes, the advantages of the present invention become clear.

N	IntDCT-IV	IntIDCT-IV	IntFFT	IntIFFT
8	0.537	0.537	0.456	0.371
16	0.546	0.546	0.480	0.412
32	0.549	0.548	0.461	0.391
64	0.550	0.550	0.462	0.393
128	0.551	0.551	0.461	0.391
256	0.552	0.552	0.461	0.391
512	0.552	0.552	0.461	0.391
1024	0.552	0.552	0.460	0.391
2048	0.552	0.552	0.461	0.391
4096	0.553	0.552	0.461	0.391

Table I

Incorporated References

The following documents are herein incorporated by reference:

H. S. Malvar, "Signal Processing with Lapped Transforms" *Artech House*, 1992;

R. Geiger, T. Sporer, J. Koller, K. Brandenburg, "Audio Coding based on Integer Transforms" *AES 111th Convention*, New York, USA, Sept. 2001;

Wang, Zhongde, "On Computing the Discrete Fourier and Cosine Transforms", IEEE Transactions on Acoustics, Speech and Signal Processing, Vol. ASSP-33, No.4 October 1985;

I. Daubechies and W. Sweldens, "Factoring wavelet transforms into lifting steps", *Tech. Report*, Bell Laboratories, Lucent Technologies, 1996;

S. Oraintara, Y. J. Chen and T. Q. Nguyen, "Integer fast Fourier transform", *IEEE Trans. Signal Processing*, vol. 50, no. 3, Mar. 2002, pp. 607-618;

P. Hao and Q. Shi, "Matrix factorizations for reversible integer mapping," *IEEE Trans. Signal Processing*, vol. 49, no. 10, Oct. 2001, pp. 2314-2324;

G. Plonka and M. Tasche, "Invertible integer DCT algorithms", *Appl. Comput. Harmon. Anal.* 15:70-88, 2003;

Y. H. Zeng, L.Z. Cheng, G. A. Bi, and Alex C. Kot, "Integer DCTs and fast algorithms", *IEEE Trans. Signal Processing*, vol. 49, no. 11, Nov. 2001, pp. 2774-2782;

J. Wang, J. Sun and S. Yu, "1-D and 2-D transforms from integers to integers", in *Proc. Int. Conf. Acoustics, Speech and Signal Processing*, Hong Kong, 2003, vol. II, pp. 549-552;

"Coding of Moving Pictures and Audio: Work plan for Evaluation of Integer MDCT for FGS to Lossless Experimentation Framework", ISO/IEC JTC 1/SC 29/WG 11 N5578, Pattaya, Thailand, Mar. 2003.